EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	85	(asynchronous same (different adj frequenc$3) same phase$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 16:55
L2	6	(asynchronous same (different adj frequenc$3) same (different adj phase$2))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 16:55
S1	1564	716/6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 11:14
S2	1966	716/5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 11:14
S3	2546	716/4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 11:14
S4	3	(716/6).ccls. and ((clock dj (environment$2 or domain$2)) same (cross$4)) and (synchron$8 same error$2) and (metastab$6) and (hold same ((set adj up) or setup) same time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 11:26
S5	1	(716/5).ccls. and ((clock dj (environment$2 or domain$2)) same (cross$4)) and (synchron$8 same error$2) and (metastab$6) and (hold same ((set adj up) or setup) same time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 11:28
S6	2	(716/4).ccls. and ((clock dj (environment$2 or domain$2)) same (cross$4)) and (synchron$8 same error$2) and (metastab$6) and (hold same ((set adj up) or setup) same time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 11:29

EAST Search History

S7	3	("716"/$).ccls. and ((clock dj (environment$2 or domain$2)) same (cross$4)) and (synchron$8 same error$2) and (metastab$6) and (hold same ((set adj up) or setup) same time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 11:29
S8	60	((clock dj (environment$2 or domain$2)) same (cross$4)) and (synchron$8 same error$2) and (metastab$6) and (hold same ((set adj up) or setup) same time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 11:30
S9	28	((clock dj (environment$2 or domain$2)) same (cross$4)) and (synchron$8 same error$2) and (metastab$6) and (hold same ((set adj up) or setup) same time) and ((adequate or proper or sufficient) adj synchroni$8)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 13:48
S10	1	((clock dj (environment$2 or domain$2)) same (cross$4)) and (synchron$8 adj error$2) and (metastab$6) and (hold same ((set adj up) or setup) same time) and ((adequate or proper or sufficient) adj synchroni$8)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 12:26
S11	0	(((clock dj (environment$2 or domain$2)) same (cross$4)) and (synchron$8 adj error$2) and (metastab$6) and (hold same ((set adj up) or setup) same time) and ((adequate or proper or sufficient) adj synchroni$8)).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 12:26
S12	1	((clock dj (environment$2 or domain$2)) same (cross$4)) and (synchron$8 adj error$2) and (metastab$6) and (hold same ((set adj up) or setup) same time) and ((adequate or proper or sufficient) adj synchroni$8)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 13:49
S13	1	((clock dj (environment$2 or domain$2)) same (cross$4)) and (metastab$6) and (hold same ((set adj up) or setup) same time) and ((synchronizer$2 or synchroniser$2) same ((adequate or proper or sufficient) adj synchroni$8))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 13:54

EAST Search History

S14	1	((clock dj (environment$2 or domain$2)) same (cross$4)) and (metastab$6) and ((synchronizer$2 or synchroniser$2) same ((adequate or proper or sufficient) adj synchroni$8))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 13:56
S15	3	(clock dj (environment$2 or domain$2)) and (metastab$6) and ((synchronizer$2 or synchroniser$2) same ((adequate or proper or sufficient) adj synchroni$8))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 14:12
S16	1	(clock dj (environment$2 or domain$2)) and (metastab$6) and ((synchronizer$2 or synchroniser$2) same ((adequate or proper or sufficient) adj synchroni$8)) and (clock$2 same frequenc$4 same phase$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 14:15
S17	2888	(asynchronous same frequenc$3 same phase$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 16:28
S18	201	synchronizer and (asynchronous same frequenc$3 same phase$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 16:29
S19	3	synchronizer and (simulat$6 same asynchronous same frequenc$3 same phase$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 16:36
S20	31	synchronizer and (simulat$6) and (asynchronous same frequenc$3 same phase$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/06 16:54

Web

Tip: Try removing quotes from your search to get more results.

Your search - **(frequency)(phase)(synchronizer)("clock domain crossing") "adequate synchronization"** - did not match any documents.

Suggestions:

- Make sure all words are spelled correctly.
- Try different keywords.
- Try more general keywords.
- Try fewer keywords.

Web Results **1** - **50** of about **146** for **(frequency)(phase)(synchronizer)("clock domain")(crossing") "adequ**

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the transmitter has the higher clock **frequency**, equiva-. lent designs can be used with the rate multiplier in the. transmitter's **clock domain**. ...
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The **synchronizer** provides a delayed ver-. sion of y in the receiver's **clock domain**. The synchro-. nizer is only active during initialization and does not ...
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probabilities of **synchronization** failure using very sim-ple hardware. ... exactly the same **frequency**,only the relative **phase** difference is unknown. This is ...
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EP1031093
thus, although the **synchronization** circuit only knows the relative **phase** ... the basic idea behind the **clock domain crossing** Circuitry is straightforward. ...
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The memory and conversion functions are in a same **clock domain**. ... When the output samples of the SRC 730 are exactly **frequency** and **phase** locked to the ...
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probability of **synchronizer** failure to acceptable levels while not introducing ... Therefore with n **clock domain crossings** a minimum latency of 4*n clock ...
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The delay associated with **crossing** a **clock domain** interface is a function ... ing loads (subject only to the relative **frequency** and **phase** of the integer and ...
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altera.com/literature/hb/qts/qts_qii5v4.pdf - Supplemental Result - Similar pages

[PDF] Section II, Design Guidelines, Stratix GX Device Handbook, Volume 3
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Results for "(((clock <and> frequency <and> phase) <and> (synchronizer <or> synchroniser)..." e-mail
Your search matched **18** of **1335860** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

☐ 1. **A maximum-likelihood-based frequency synchronizer for dual-h continuc modulation (CPM)**
Pettit, R.H.; Wahlen, B.E.; Mai, C.Y.; North, R.C.;
Military Communications Conference, 2001. MILCOM 2001. Communications f Centric Operations: Creating the Information Force. IEEE
Volume 2, 28-31 Oct. 2001 Page(s):1219 - 1223 vol.2
Digital Object Identifier 10.1109/MILCOM.2001.986044
AbstractPlus | Full Text: PDF(788 KB) IEEE CNF
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☐ 2. **High resolution multi-frequency digital phase locked loop**
Efendovich, A.; Afek, Y.; Sella, C.; Bikowsky, Z.;
Circuits and Systems, 1993., ISCAS '93, 1993 IEEE International Symposium (3-6 May 1993 Page(s):1128 - 1131 vol.2
Digital Object Identifier 10.1109/ISCAS.1993.393903
AbstractPlus | Full Text: PDF(336 KB) IEEE CNF
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☐ 3. **Analysis of true jitter arising from pulse-stuffing schemes**
Abeysekera, S.S.;
Communications, IEEE Transactions on
Volume 51, Issue 4, April 2003 Page(s):597 - 606
Digital Object Identifier 10.1109/TCOMM.2003.810802
AbstractPlus | References | Full Text: PDF(709 KB) IEEE JNL
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☐ 4. **A research on adaptive PCM clock recovering**
Wu Zhilu; Yin Zhendong; Ren Guanghui; Yang Shuiwang;
Microwave and Millimeter Wave Technology, 2002. Proceedings. ICMMT 2002 International Conference on
17-19 Aug. 2002 Page(s):406 - 409
AbstractPlus | Full Text: PDF(283 KB) IEEE CNF
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☐ 5. **Joint carrier phase and symbol timing synchronization for burst satellite**
Yigang Fan; Chakravarthi, P.;
MILCOM 2000. 21st Century Military Communications Conference Proceeding
Volume 2, 22-25 Oct. 2000 Page(s):1104 - 1108 vol.2

Results for "(((clock <and> crossing) <and> (synchronizer <or> synchroniser))<in>metadata..." e-mail
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(((clock <and> crossing) <and> (synchronizer <or> synchroniser))<in>metadata) Search

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IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

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1. **A fast resolving BiNMOS synchronizer for parallel processor interconnec**
Jex, J.; Dike, C.;
Solid-State Circuits, IEEE Journal of
Volume 30, Issue 2, Feb. 1995 Page(s):133 - 139
Digital Object Identifier 10.1109/4.341740
AbstractPlus | Full Text: PDF(528 KB) IEEE JNL
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2. **Timing measurements of synchronization circuits**
Semiat, Y.; Ginosar, R.;
Asynchronous Circuits and Systems, 2003. Proceedings. Ninth International S
12-15 May 2003 Page(s):68 - 77
Digital Object Identifier 10.1109/ASYNC.2003.1199167
AbstractPlus | Full Text: PDF(435 KB) IEEE CNF
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3. **Cross-layer sensor network synchronization**
Tian, Z.; Luo, X.; Giannakis, G.B.;
Signals, Systems and Computers, 2004. Conference Record of the Thirty-Eigh Conference on
Volume 1, 7-10 Nov. 2004 Page(s):1276 - 1280 Vol.1
Digital Object Identifier 10.1109/ACSSC.2004.1399347
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(((clock domain crossing) <and> (synchronizer <or> synchroniser) <and> (adequate s Search

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IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

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Check to search only within this results set

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» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

No results were found.

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Search Results

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Your search matched **0** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

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(((clock domain crossing) <and> (synchronizer <or> synchroniser))<in>metadata) Search

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IEEE JNL	IEEE Journal or Magazine
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